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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/754,806	01/02/2001	Q.Z. Liu	00CON122P-DIV1	2716
25700	7590	02/15/2005	EXAMINER	
FARJAMI & FARJAMI LLP 26522 LA ALAMEDA AVENUE, SUITE 360 MISSION VIEJO, CA 92691			NADAV, ORI	
			ART UNIT	PAPER NUMBER
			2811	

DATE MAILED: 02/15/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/754,806

Applicant(s)

LIU ET AL

Examiner

ori nadav

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— The MAILING DATE of this communication appears on the cover sheet with the correspondence address —

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 08 November 2004.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 24-26 and 28-48 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 24-26 and 28-48 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 24-26, 28, 31-34, 37, 39-43 and 48 are rejected under 35 U.S.C. 102(e) as being anticipated by El-Sharawy et al. (6,013,939).

El-Sharawy et al. teach in figure 1 and related text a structure in a semiconductor chip, the structure comprising

a first area of dielectric 34 having a first permeability,

a second area of dielectric 32 having a second permeability, wherein the second permeability is higher than the first permeability

a permeability conversion magnetic oxide material 32 having a second permeability, the permeability conversion material 32 being interspersed within the second area of said dielectric, wherein a second permeability being achieved by interspersing a permeability conversion material (metal particles) within the second area of the dielectric, the permeability conversion material having a third permeability, the third permeability being greater than the first and second permeabilities,

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a conductor 38 patterned in said second area of the dielectric, said permeability conversion material not being situated underneath the conductor, wherein said dielectric is not situated underneath and not situated over the conductor, and wherein the conductor having first and second terminals, the first and second terminals of the conductor being respectively first and second terminals of the inductor.

Although El-Sharawy et al. do not state that the second permeability of magnetic oxide layer 32 is higher than that of the first permeability of layer 34, this feature is inherent in El-Sharawy et al.'s device, because it is well known that the permeability of magnetic oxide is higher than that of the silicon oxide.

Regarding claims 31 and 37, El-Sharawy et al. teach a conductor being an inductor, because section 36 is the center of the inductor (coil), and thus layer 38 is part of the inductor.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 29-30, 35-36, 38 and 44-47 are rejected under 35 U.S.C. 103(a) as being unpatentable over El-Sharawy et al.

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Regarding claims 29, 35 and 46, El-Sharawy et al. do not teach using a conductor being selected from the group consisting of copper, aluminum, and copper-aluminum alloy. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to use a conductor being selected from the group consisting of copper, aluminum, and copper-aluminum alloy in Yokogawa 's device in order to improve the conductivity of the device with a conventional conducting material. Note that substitution of materials is not patentable even when the substitution is new and useful. *Safetran Systems Corp. v. Federal Sign & Signal Corp.* (DC NIII, 1981) 215 USPQ 979.

Regarding the processing limitations recited in claims 38, 44 and 45 ("the permeability conversion material is interspersed in the second dielectric area by ion implantation and by sputtering when the first dielectric area is covered with photo resist"), these would not carry patentable weight in this claim drawn to a structure, because distinct structure is not necessarily produced. Note that a "product by process" claim is directed to the product per se, no matter how actually made, *In re Hirao*, 190 USPQ 15 at 17 (footnote 3). See also *In re Brown*, 173 USPQ 685; *In re Luck*, 177 USPQ 523; *In re Fessmann*, 180 USPQ 324; *In re Avery*, 186 USPQ 161; *In re Wertheim*, 191 USPQ 90 (209 USPQ 554 does not deal with this issue); and *In re Marosi et al.*, 218 USPQ 289, all of which make it clear that it is the patentability of the final product per se which must be determined in a "product by process" claim, and not the patentability of the process, and that an old or obvious product produced by a new method is not patentable as a

product, whether claimed in "product by process" claims or not. Note that the applicant has the burden of proof in such cases, as the above case law makes clear.

Claims 30, 36 and 47 are rejected under 35 U.S.C. 103(a) as being unpatentable over El-Sharawy et al. in view of Yokogawa (Jp 402262308A).

El-Sharawy et al. teach substantially the entire claimed structure, as recited in claims 24, 31 and 37, except a conductor patterned as a square spiral. Yokogawa teach in figure 1 a conductor patterned as a square spiral. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to use a conductor patterned as a square spiral in El-Sharawy et al.'s device in order to simplify the processing steps of making the device by using conventional square spiral inductor.

Claims 31-36 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yokogawa (Jp 402262308A) in view of Cornett et al. (6,069,397) and Ewen et al. (5,446,311).

Yokogawa teaches in figure 2 and related text a structure in a semiconductor chip, the structure comprising a first area of dielectric 4 (the first area of dielectric 4 is selected to be the area located above the white dielectric layer in the center of the structure, wherein two inductors 3 are located on both sides of the first area) having a first permeability, a second area of dielectric (the white dielectric area located in between inductor 3) having a second permeability, an inductor 3 comprising a square spiral (see figure 1) conductor patterned within the dielectric, patterned in the second area of the dielectric, wherein the material of the second area of the dielectric not being situated

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underneath the conductor, the first area of the dielectric not being situated underneath and not being situated over the conductor and the second area of the dielectric not being situated over the conductor, and wherein the conductor having first and second terminals, the first and second terminals of the conductor being respectively first and second terminals of the inductor.

Yokogawa does not teach the material of the first area of dielectric 4 and the material of the second area of dielectric.

Cornett et al. teach in figure 2 and related text a structure in a semiconductor chip, the structure comprising a dielectric 217 having a first permeability, a permeability conversion magnetic oxide material 223 having a second permeability, the permeability conversion material (metal) being interspersed within the dielectric, wherein the second permeability is greater than the first permeability (column 2, lines 39-62), wherein a second permeability being achieved by interspersing a permeability conversion material (metal particles) within the second area of the dielectric, the permeability conversion material having a third permeability, the third permeability being greater than the first and second permeabilities, an inductor 110 comprising a square spiral (see figure 1) conductor patterned within the dielectric, wherein the permeability conversion material 223 not being situated underneath the conductor, the conductor having first and second terminals, the first and second terminals of the conductor being respectively first and second terminals of the inductor.

Cornett et al. do not explicitly state that the second permeability of magnetic oxide layers 221, 223. is greater than the first permeability of passivation/dielectric layer 217.

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That is, Cornett et al. do not state that the conventional passivation/dielectric layer 217 comprise silicon oxide. Ewen et al. teach in figure 3 a passivation/dielectric layer 2 comprising silicon oxide.

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to use silicon oxide as the material for the first area of dielectric 4 and magnetic oxide as the material of the second area of dielectric (the white area between inductor 4) in Yokogawa's device, as taught by Cornett et al., in order to simplify the processing the steps of the making the device by insulating the device with a conventional silicon oxide insulating material, and in order to improve the magnetic characteristics of the inductor, respectively.

Regarding claim 35, Yokogawa does not teach using a conductor being selected from the group consisting of copper, aluminum, and copper-aluminum alloy. It would have been obvious to a person of ordinary skill in the art at the time the invention was made to use a conductor being selected from the group consisting of copper, aluminum, and copper-aluminum alloy in Yokogawa's device in order to improve the conductivity of the device with a conventional conducting material. Note that substitution of materials is not patentable even when the substitution is new and useful. *Safetran Systems Corp. v. Federal Sign & Signal Corp.* (DC NIII, 1981) 215 USPQ 979.

Response to Arguments

Applicant's arguments with respect to claims 24-26 and 28-48 have been considered but are moot in view of the new ground(s) of rejection.

Papers related to this application may be submitted to Technology center (TC) 2800 by facsimile transmission. Papers should be faxed to TC 2800 via the TC 2800 Fax center located in Crystal Plaza 4, room 4-C23. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (November 15, 1989). The Group 2811 Fax Center number is (703) 308-7722 and 308-7724. The Group 2811 Fax Center is to be used only for papers related to Group 2811 applications.

Any inquiry concerning this communication or any earlier communication from the Examiner should be directed to *Examiner Nadav* whose telephone number is **(571) 272-1660**. The Examiner is in the Office generally between the hours of 7 AM to 4 PM (Eastern Standard Time) Monday through Friday.

Any inquiry of a general nature or relating to the status of this application should be directed to the **Technology Center Receptionists** whose telephone number is **308-0956**



O.N.
February 9, 2005

ORI NADAV
PRIMARY EXAMINER
TECHNOLOGY CENTER 2800